

37 and 15

	U	Inventor	Document/Issue P	Title	Curran	Current X	Retrieval	S	C	P	Image Do P
1	<input type="checkbox"/>	Dennison, US	675928:2004	2 Methods of forming transistors	438/15	438/153			<input type="checkbox"/>	<input type="checkbox"/>	US 675928
2	<input type="checkbox"/>	Sin, Johnn US	588200:1999	1 Polysilicon devices and a method for f	257/34	257/316			<input type="checkbox"/>	<input type="checkbox"/>	US 588200
3	<input type="checkbox"/>	Hackler, Do US	200500:2005	2 Sram cell	438/25				<input type="checkbox"/>	<input type="checkbox"/>	US 200500
4	<input type="checkbox"/>	Parke, Step US	658013:2003	3 Demascene double gated transistors	257/40	257/407			<input type="checkbox"/>	<input type="checkbox"/>	US 658013
5	<input type="checkbox"/>	Shepard, J US	557429:1996	8 Vertical dual gate thin film transistor w	257/66	257/327			<input type="checkbox"/>	<input type="checkbox"/>	US 557429
6	<input type="checkbox"/>	Madurawe, US	200500:2005	2 Semiconductor switching devices	365/19				<input type="checkbox"/>	<input type="checkbox"/>	US 200500
7	<input type="checkbox"/>	Shionoi, Y US	200500:2005	5 Sense amplifier and electronic apparat	327/85				<input type="checkbox"/>	<input type="checkbox"/>	US 200500
8	<input type="checkbox"/>	Koyama, J US	200402:2004	5 Liquid crystal display device and meth	345/87				<input type="checkbox"/>	<input type="checkbox"/>	US 200400
9	<input type="checkbox"/>	Madurawe, US	200402:2004	3 Semiconductor latches and SRAM dev	438/20				<input type="checkbox"/>	<input type="checkbox"/>	US 200400
10	<input type="checkbox"/>	Lee, Thom US	200402:2004	1 Rail stack array of charge storage dev	438/14				<input type="checkbox"/>	<input type="checkbox"/>	US 200400